

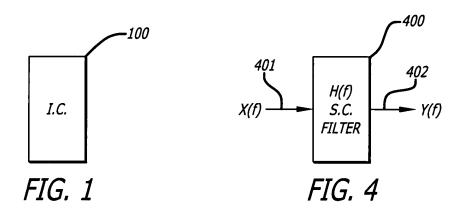
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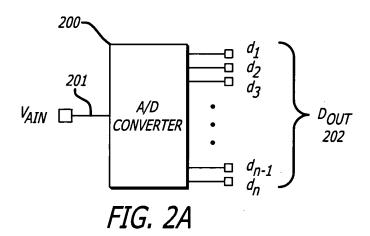
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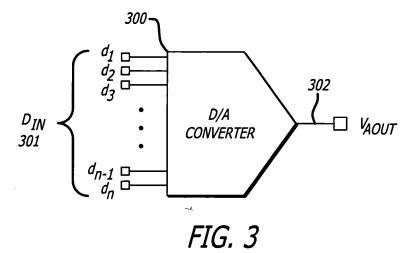
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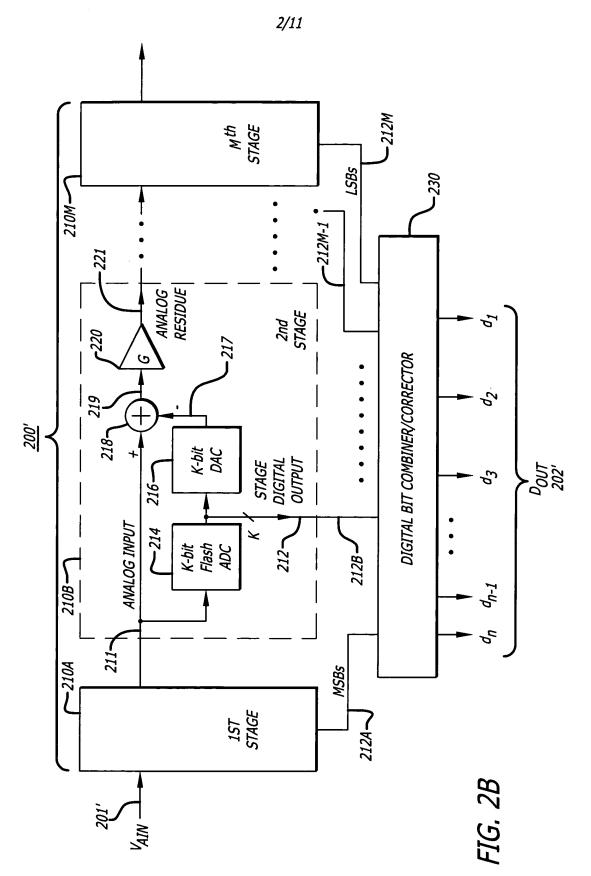


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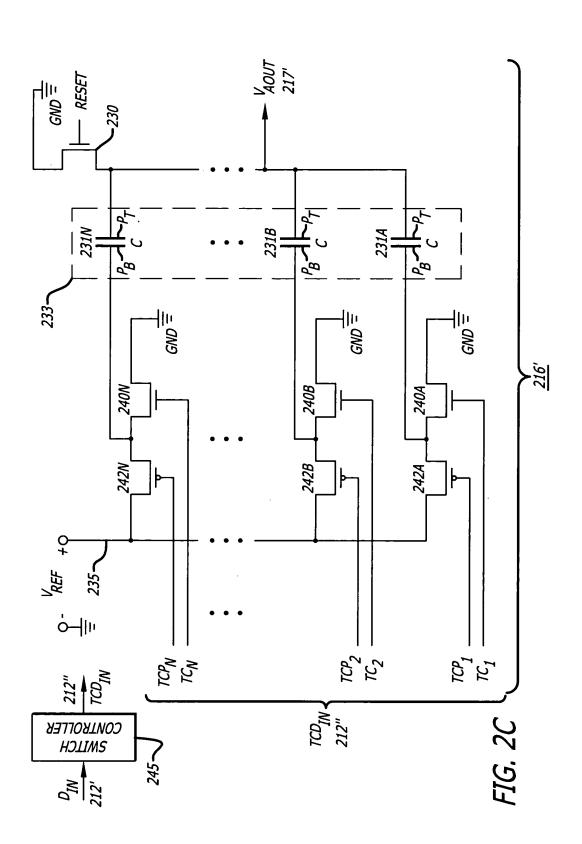
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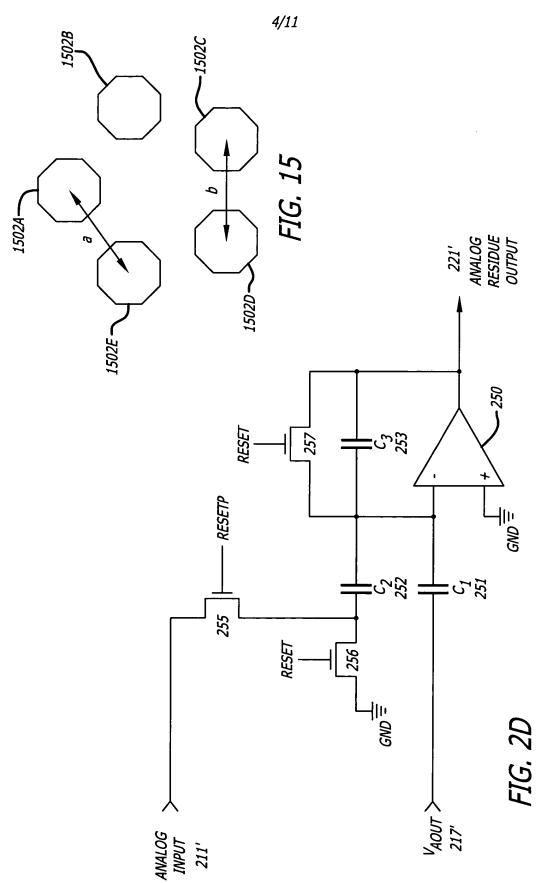
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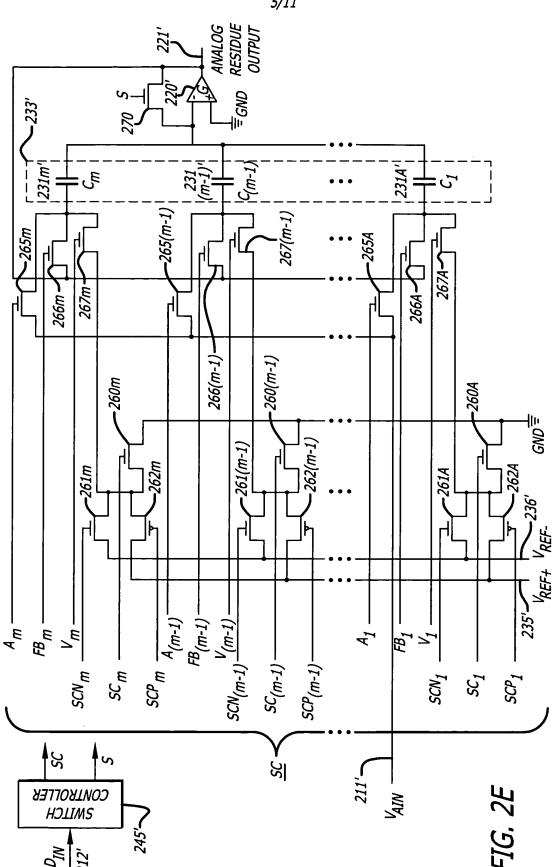
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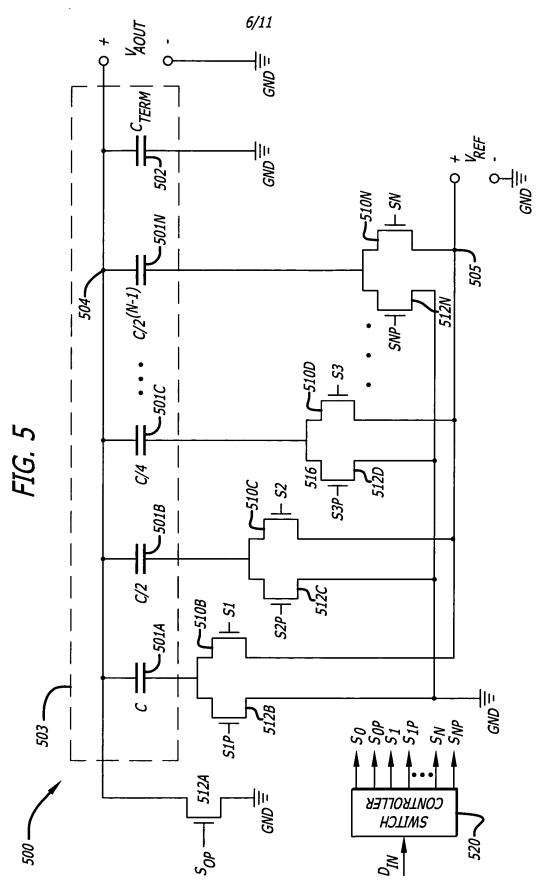
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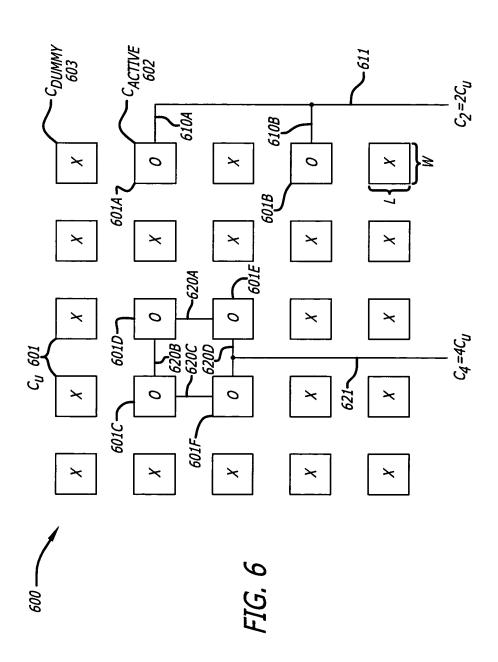


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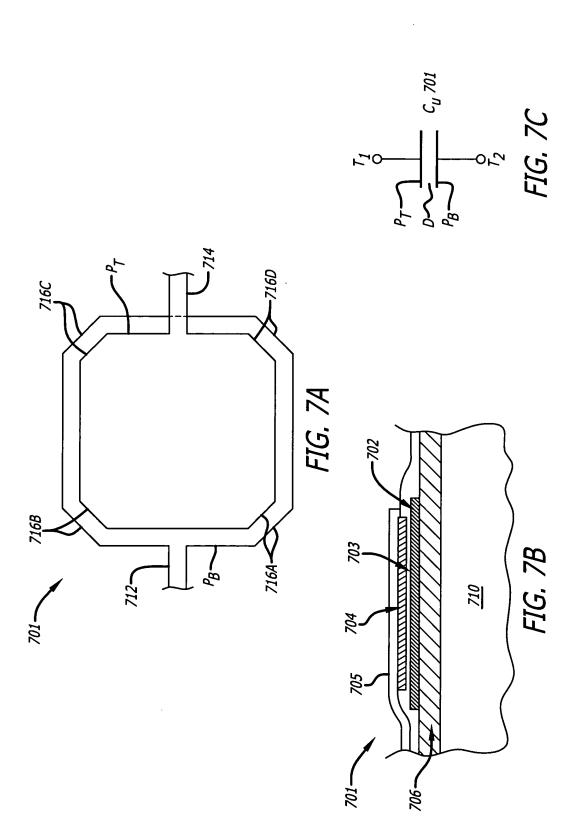
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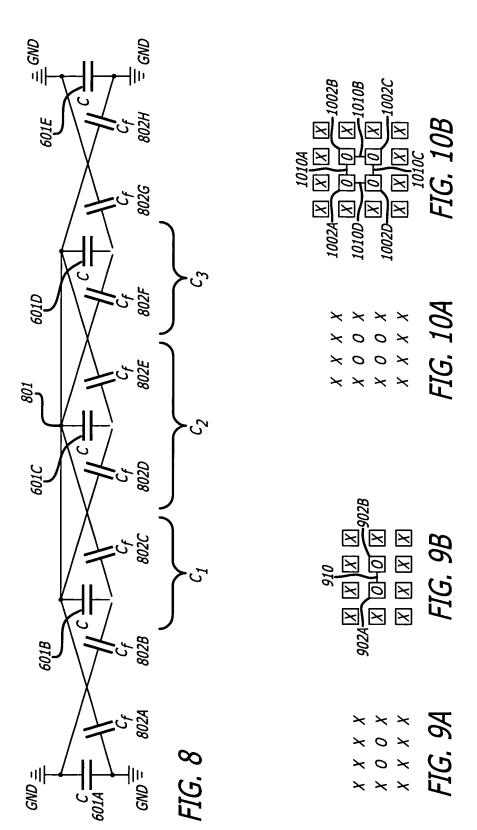
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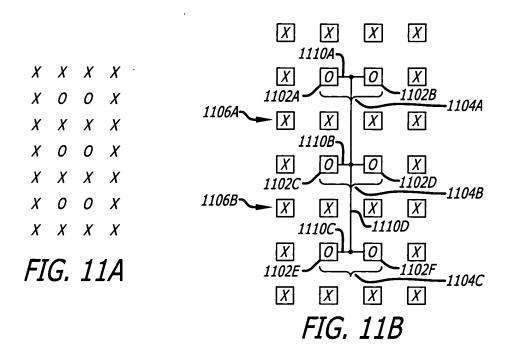
1st Named Inventor: Juha Mikko Hakkarainen

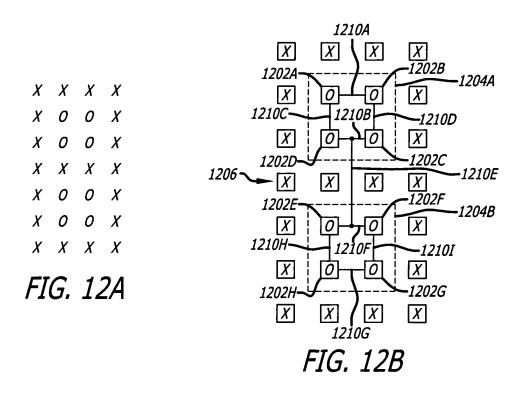
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